



**INSTITUTE OF TECHNOLOGY**  
**University of Kashmir**  
Zakura Campus, Srinagar 190006

No. F (Date Sheet, 6<sup>th</sup> Semester/ECE) 101/KU/2018  
Dated: 15/07/2019


**Date Sheet**

**End Semester Examination for Regular/Backlog Candidates  
Of B.Tech, ECE 6<sup>th</sup> Semester, Zakura Campus.**

**Time:** 11:00 A.M.

**Venue:** Block B. 1st Floor (CBF1 & CBF2)

Date/ Day	Course Name/Course Code
22/07/2019 (Monday)	<i>Power Electronics (ECE6517B)</i>
24/07/2019 (Wednesday)	<i>Microcontrollers &amp; Embedded Systems(ECE6217B)</i>
26/07/2019 (Friday)	<i>Computer Organization &amp; Architecture (ECE6317B ) &amp; Data Structure(CSE6517)</i>
29/07/2019 (Monday)	<i>Communication Systems-II(ECE6117B)</i>
31/07/2019 (Wednesday)	<i>Electronic Measurements &amp; Instrumentation(ECE6417B) &amp; VLSI Design(ECE6417)</i>

  
Coordinator ECE

Copy To:

1. Controller of Examinations, University of Kashmir.
2. Director, Institute of Technology, University of Kashmir
3. Dean, Faculty of Engineering, University of Kashmir.
4. Website Incharge (Mr. Basharat)
5. Notice Board.
6. File.