



**Department of Electrical Engineering
Institute of Technology
University of Kashmir
Zakura Campus
Srinagar**

Address for
Correspondence:
Coordinator, Department
of Electrical Engineering
Institute of Technology
University of Kashmir
Zakura, Srinagar –
190006, India(2024)
e-mail id:
ioteee@uok.edu.in

Date sheet for group wise presentations of Digital Electronics, 4th semester(Batch 2022)

Venue:First floor, Block C, Institute of Technology, Zakura

DATE	GROUP	ROLL NUMBER
20-05-2024 TIME- 2:00PM	GROUP:1-6	G1- 02,03,04 G2-05,06,07 G3-08,09,10 G4-11,12,13 G5-14,15,17 G6-18,19,20
27-05-2024 TIME- 2:00PM	GROUP:7-12	G7-21,22,23 G8-24,26,27 G9-28,29,30 G10-32,33,34 G11-35,36,37 G12-38,39,41
03-06-2024 TIME- 2:00PM	GROUP:13-19	G13-42,43,44 G14-45,47,48 G15- 49,50,51 G16-52,53,55 G17-56,57,58 G18-59,60,61 G19-62,63,64

TOPICS-

- ROM, EPROM ,RAM - Group 1, 6, 11,3 and 16
- RTL, DCTL, DTL - Group 2, 7, 12 , 8 and 17
- TTL, ECL AND MOS LOGIC FAMILIES- Group 4, 9 ,14 , 13 and 19
- TTL SUB FAMILIES AND COMPARISON OF DIFFERENT LOGIC FAMILIES-
Group 5,10, 18 and15

-SD-

**Coordinator
Department of Electrical Engineering**